***ECE 232 LAB 11***

*CIRCUIT STATE-SPACE MODELING*



*Find a state space representation for the RLC circuit shown in Fig and assuming the output to be* $V\_{out}$ *and the state vectors*

$X\_{1}(t)$*=voltage across the capacitor ;*

$X\_{2}(t)$*=inductor current ;*

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*EXPERIMENTAL PART:*

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*Plot the* $V\_{out }$*vs* $V\_{in}$ *and show the Voltages,Peak to Peak ,Time/Div ,Volt/Div, probe vs..*

*Voltages:\_\_\_\_*

*P-p=\_\_\_\_\_*

*Time/Div=\_\_\_\_*

*Volt/Div=\_\_\_\_*

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